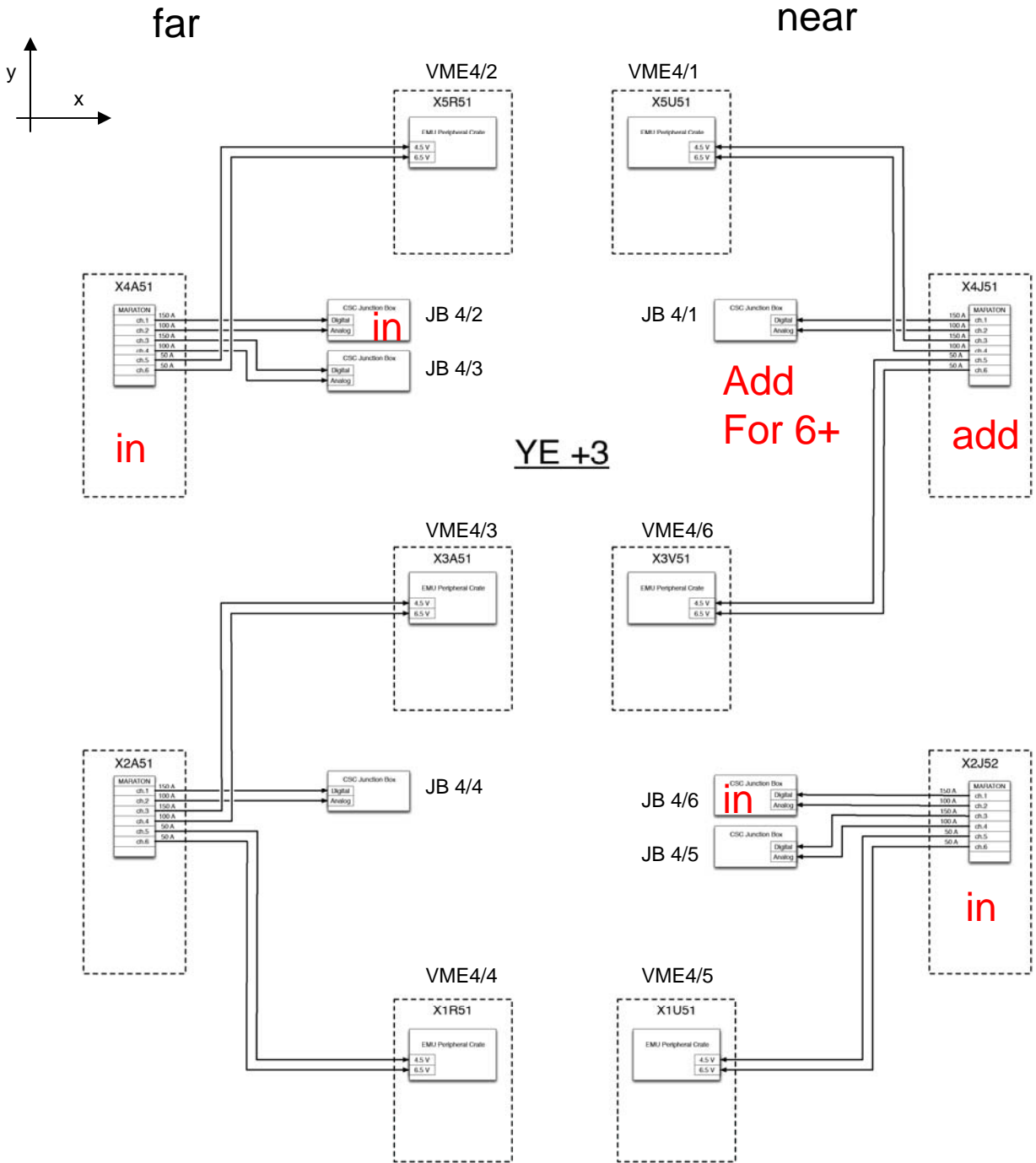
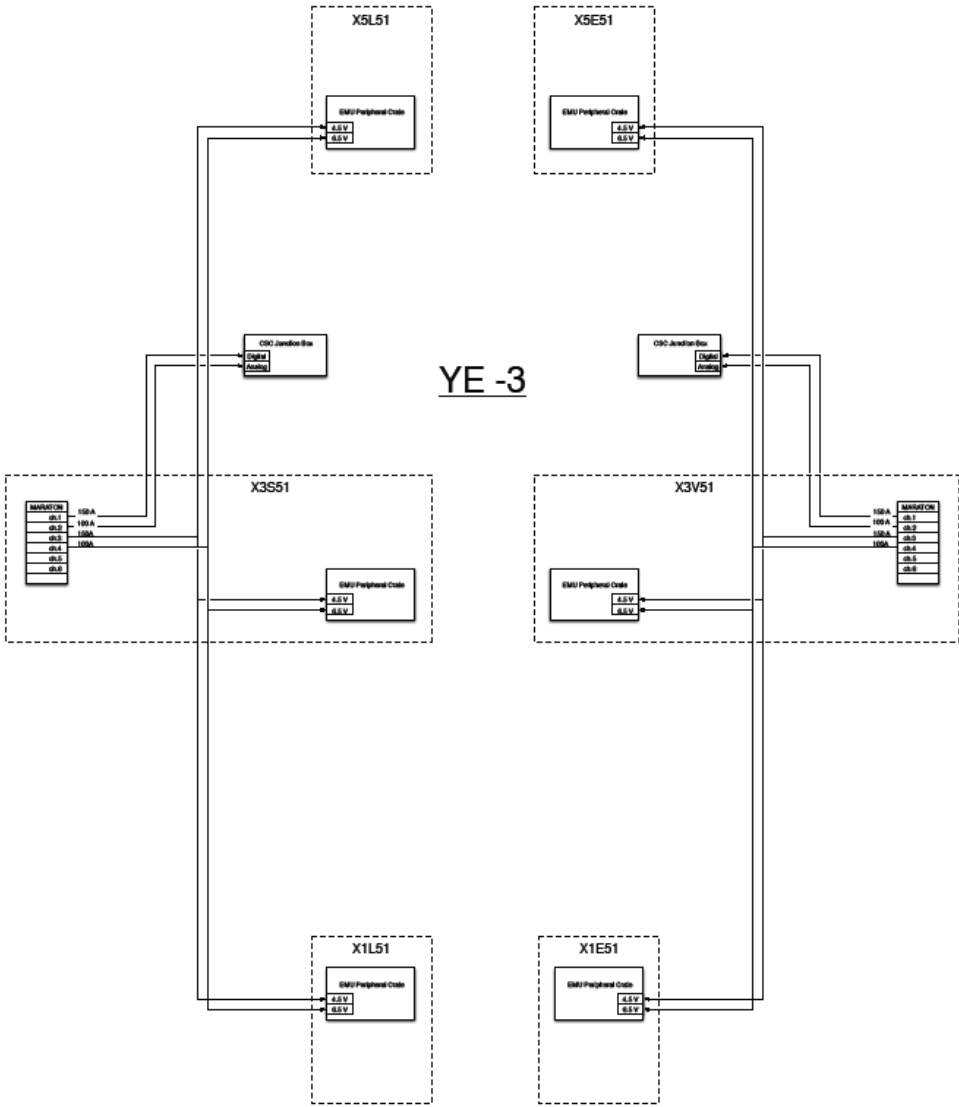


# YE3 ME4/2



# YE3 As Built



# YE3 ME4/2 upgrade

YE+3 As Built										LV Cables vs Maraton Channels, YE+3					
										50 A	50 A	100 A	100 A	150 A	150 A
Rack Name	DCS Nan	PFC Crate	Mod.	Qua.	Side	Lev.	ID	Position	Name	5	1	7	3	4	0
X1A51a	1_IV_MIS4F04k		1	2	-X	4	17	X1A51	IVPS+4/2/1			PC+4/2 - 6.5V	JB+4/2 - AN	PC+4/2 - 4.5V	JB+4/2 - DIG
												PC+4/3 - 6.5V	JB+4/3 - AN	PC+4/3 - 4.5V	JB+4/3 - DIG
												PC+4/4 - 6.5V	JB+4/4 - AN	PC+4/4 - 4.5V	JB+4/4 - DIG
X2J52b	2_IV_MIS4F04k		2	4	+X	2	18	X2J52	IVPS+4/4/1			PC+4/1 - 6.5V	JB+4/5 - AN	PC+4/1 - 4.5V	JB+4/5 - DIG
												PC+4/6 - 6.5V	JB+4/6 - AN	PC+4/6 - 4.5V	JB+4/6 - DIG
												PC+4/5 - 6.5V	JB+4/1 - AN	PC+4/5 - 4.5V	JB+4/1 - DIG

YE+3 ME4_2 Upgrade										LV Cables vs Maraton Channels, YE+3 Prime					
										50 A	50 A	100 A	100 A	150 A	150 A
Rack Name	DCS Nan	PFC Crate	Mod.	Qua.	Side	Lev.	ID	Position	Name	5	1	7	3	4	0
X1A51a	4_IV_MIS4F04k		3	2	-X	4	12	X1A51_L	IVPS+4/2/1	PC+4/2 - 6.5V	PC+4/2 - 4.5V	JB+4/3 - AN	JB+4/2 - AN	JB+4/3 - DIG	JB+4/2 - DIG
X2A51a	6_IV_MIS4F04k		5	3	-X	2	14	X2A51_L	IVPS+4/3/1	PC+4/3 - 6.5V	PC+4/3 - 4.5V	PC+4/4 - 6.5V	JB+4/4 - AN	PC+4/4 - 4.5V	JB+4/4 - DIG
X2J52a	7_IV_MIS4F04i		1	4	+X	2	15	X2J52_L	IVPS+4/4/1	PC+4/5 - 6.5V	PC+4/5 - 4.5V	JB+4/5 - AN	JB+4/6 - AN	JB+4/5 - DIG	JB+4/6 - DIG
X4J51a	1_IV_MIS4F04i		3	1	+X	4	9	X4J51_L	IVPS+4/1/1	PC+4/6 - 6.5V	PC+4/6 - 4.5V	PC+4/1 - 6.5V	JB+4/1 - AN	PC+4/1 - 4.5V	JB+4/1 - DIG

NOTE: JB+4/n is for station 4 TS n and is next to the Maraton in column A(J)

Proposed layout on IVPS for the full upgraded ME4/2 system

YE+3 hybrid >> TS1										LV Cables vs Maraton Channels, YE+3					
										50 A	50 A	100 A	100 A	150 A	150 A
Rack Name	DCS Nan	PFC Crate	Mod.	Qua.	Side	Lev.	ID	Position	Name	5	1	7	3	4	0
X1A51a	1_IV_MIS4F04k		1	2	-X	4	17	X1A51	IVPS+4/2/1			PC+4/2 - 6.5V	JB+4/2 - AN	PC+4/2 - 4.5V	JB+4/2 - DIG
												PC+4/3 - 6.5V	JB+4/3 - AN	PC+4/3 - 4.5V	JB+4/3 - DIG
												PC+4/4 - 6.5V	JB+4/4 - AN	PC+4/4 - 4.5V	JB+4/4 - DIG
X2J52b	2_IV_MIS4F04k		2	4	+X	2	18	X2J52	IVPS+4/4/1				JB+4/5 - AN		JB+4/5 - DIG
												PC+4/6 - 6.5V	JB+4/6 - AN	PC+4/6 - 4.5V	JB+4/6 - DIG
												PC+4/5 - 6.5V		PC+4/5 - 4.5V	
X4J51a	1_IV_MIS4F04i		3	1	+X	4	9	X4J51_L	IVPS+4/1/1			PC+4/1 - 6.5V	JB+4/1 - AN	PC+4/1 - 4.5V	JB+4/1 - DIG

Proposed midpoint step for the addition of 6 chambers in only TS1

For this layout a new Maraton is placed in its final rack, minimal changes are needed to install and operate.

# Maraton Output Channel Mapping

•	<u>Maraton</u>		<u>Sergei Drawing</u>		<u>DCS</u>
•	0	150A	1	digital or 4.5V	1
•	3	100A	3	analog or 6.5V	2
•	4	150A	4	digital or 4.5V	3
•	7	100A	6	analog or 6.5V	4
•	1	50A	2	4.5V	5
•	5	50A	5	6.5V	6